Bharat Sanchar Nigam Limited
Recruitment Section
Janpath, New Delhi -110001

No. 1-4/2011-R-II

To

All CGMT

......................Circle BSNL.

Sub: Holding of Special Recruitment Drive (SRD) for filling up backlog vacancies of Junior Telecom Officer (Telecom/Civil/Electrical) for Persons with Disabilities (PWD) -JTO (SRD) 2011.

Sir,


2. Approval of the competent authority is hereby conveyed to conduct SRD for filling up backlog vacancies reserved for PWDs in the cadre of JTO in Telecom, Electrical and Civil streams under Direct Recruitment quota. The numbers of backlog vacancies which may be filled up by conducting SRD Exam as intimated by the circles are given in the Annexure.

3. As per 16 (c) of DoPT OM No. 36035/3/2004-Estt (Res) dated 29.12.2005, the vacancies are interchangeable i.e., if vacancy of one category of disability remains unfilled, it may be filled by person of other category of disability.

4. The date of JTO SRD Exam-2011 has been fixed as 19-2-2012. The examination shall be conducted in accordance with the terms and conditions viz. educational qualification, age, scheme and syllabus of the competitive examination etc. as adopted in JTO SRD Exam 2010 held on 19-12-2010.
5. In the cadre of JTO (Telecom/Civil/Electrical) the physically handicapped persons having the following disability have been permitted:

   i) Hearing Impairment-Partially Deaf

   ii) Locomotors Impairment-One arm or one leg or both legs affected.

       (As per Central Govt. Guidelines, the minimum degree of disability is 40% in order for a person to be eligible for any concession/benefits).

6. It has been decided with the approval of competent authority that:-

   a) All recruiting circles will issue the advertisements separately for their respective circle similar to the advertisement issued for the earlier JTO SRD-2010 exam. **In the advertisement, it may be indicated that Exam is to be conducted on 19th February, 2012.**

   b) The paper setting and printing will be arranged by Recruitment Cell of BSNL CO in order to have same standard throughout India.

   c) The Examination will be OMR based as in the past and OMR sheets shall also be arranged by Recruitment Cell, BSNL CO.

   d) After the examination is conducted, the Provisional Answer Keys of JTO SRD Exam of all three streams will be uploaded on the BSNL website (www.bsnl.co.in) in order to bring transparency and invite comments, if any, from the candidates by e-mail at E-mail Id recttcell@bsnl.co.in within a period of 10 days after uploading of Answer keys. The Provisional Answer Key will be finalised after considering the representation/feedback received. Final answer key will then be provided to the circles for evaluation of OMR Answer sheets.

   e) Evaluation of OMR sheets will be got done by the respective circle as per the instructions issued vide our letter no. 1-5/2010/R-11 dated 1-12-2010.

   f) Respective Recruiting circle will prepare and declare the results.

7. The Circle-wise distribution of vacancies shall be done by the respective Recruiting Circle after taking into account the demands of Non-recruiting circles like projects, Maintenance Regions etc. The corporate Office may be kept informed about the No. of candidates who join as JTO through SRD Exam.
Regarding educational qualifications in various streams (Telecom/Civil/Elect), it may be mentioned in the advertisement that applicant must possess as on 31-12-2011 the required degree in the respective discipline.

10. Respective circle may explore the possibility of online registration of applications, e-admit card, declaration of result through website etc. as was done in case of JAO (DR) and SDE LDCE for the post of SDE(T) exams.

11. The broad activities involved in conducting JTO SRD Exam 2011 are detailed below alongwith the time schedule:-

<table>
<thead>
<tr>
<th>S No.</th>
<th>Activities</th>
<th>To be completed by</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Circle to release Advertisement</td>
<td>21-11-2011</td>
</tr>
<tr>
<td>2</td>
<td>Closing date of receipt of application</td>
<td>12-12-2011</td>
</tr>
<tr>
<td>3</td>
<td>Date of conduct of Exam.</td>
<td>19-02-2012</td>
</tr>
<tr>
<td>4</td>
<td>Date of declaration of Result</td>
<td>19-03-2012</td>
</tr>
</tbody>
</table>

The above time schedule should be strictly adhered to by the circles.

(Bindu Roy)
AGM (Rectt)
Tele No. 23708041

Copy to: 1. GM (Estt), BSNL, CO.
         2. CLO (SCT Cell), BSNL, CO.
### Annexure-A

#### Circle-wise and Category-wise vacancies of JTOs (Telecom)

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Name of Circle</th>
<th>Loco-motor Impairment Category</th>
<th>Hearing Impairment Category</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>AP</td>
<td>00</td>
<td>08</td>
</tr>
<tr>
<td>2.</td>
<td>A&amp;N</td>
<td>01</td>
<td>00</td>
</tr>
<tr>
<td>3.</td>
<td>Assam</td>
<td>02</td>
<td>05</td>
</tr>
<tr>
<td>4.</td>
<td>Bihar</td>
<td>03</td>
<td>03</td>
</tr>
<tr>
<td>5.</td>
<td>Kolkata TD</td>
<td>04</td>
<td>06</td>
</tr>
<tr>
<td>6.</td>
<td>Chhattisgarh</td>
<td>01</td>
<td>01</td>
</tr>
<tr>
<td>7.</td>
<td>Gujarat</td>
<td>11</td>
<td>14</td>
</tr>
<tr>
<td>8.</td>
<td>Haryana</td>
<td>02</td>
<td>04</td>
</tr>
<tr>
<td>9.</td>
<td>HP</td>
<td>02</td>
<td>03</td>
</tr>
<tr>
<td>10.</td>
<td>Chennai TD</td>
<td>00</td>
<td>01</td>
</tr>
<tr>
<td>11.</td>
<td>J&amp;K</td>
<td>01</td>
<td>04</td>
</tr>
<tr>
<td>12.</td>
<td>Jharkhand</td>
<td>00</td>
<td>01</td>
</tr>
<tr>
<td>13.</td>
<td>Karnataka</td>
<td>09</td>
<td>05</td>
</tr>
<tr>
<td>14.</td>
<td>Kerala</td>
<td>01</td>
<td>04</td>
</tr>
<tr>
<td>15.</td>
<td>MP</td>
<td>01</td>
<td>17</td>
</tr>
<tr>
<td>16.</td>
<td>Maharashtra</td>
<td>12</td>
<td>01</td>
</tr>
<tr>
<td>17.</td>
<td>NTR</td>
<td>00</td>
<td>03</td>
</tr>
<tr>
<td>18.</td>
<td>NE-I</td>
<td>01</td>
<td>01</td>
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<tr>
<td>19.</td>
<td>NE-II</td>
<td>00</td>
<td>03</td>
</tr>
<tr>
<td>20.</td>
<td>Orissa</td>
<td>01</td>
<td>08</td>
</tr>
<tr>
<td>21.</td>
<td>Punjab</td>
<td>05</td>
<td>03</td>
</tr>
<tr>
<td>22.</td>
<td>Rajasthan</td>
<td>00</td>
<td>07</td>
</tr>
<tr>
<td>23.</td>
<td>Tamilnadu</td>
<td>01</td>
<td>01</td>
</tr>
<tr>
<td>24.</td>
<td>Uttaranchal</td>
<td>00</td>
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</tr>
<tr>
<td>25.</td>
<td>UP(E)</td>
<td>00</td>
<td>01</td>
</tr>
<tr>
<td>26.</td>
<td>UP(W)</td>
<td>00</td>
<td>03</td>
</tr>
<tr>
<td>27.</td>
<td>West Bengal</td>
<td>07</td>
<td>07</td>
</tr>
<tr>
<td></td>
<td>Total</td>
<td>65</td>
<td>129</td>
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## Annexure-B

### Circle-wise and Category-wise vacancies of JTOs (Civil)

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Circle</th>
<th>Civil</th>
<th>Loco-motor Impairment (LI)</th>
<th>Hearing Impairment (HI)</th>
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</thead>
<tbody>
<tr>
<td>1.</td>
<td>MP</td>
<td></td>
<td>00</td>
<td>01</td>
</tr>
<tr>
<td>2.</td>
<td>NTR (Delhi)</td>
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<td>00</td>
<td>01</td>
</tr>
<tr>
<td>3.</td>
<td>Punjab</td>
<td></td>
<td>01</td>
<td>00</td>
</tr>
<tr>
<td>4.</td>
<td>West Bengal</td>
<td></td>
<td>00</td>
<td>01</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td></td>
<td>01</td>
<td>03</td>
</tr>
</tbody>
</table>
## Circle-wise vacancies of JTOs (Electrical)

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Name of Circle</th>
<th>Vacancies</th>
</tr>
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<tbody>
<tr>
<td>1.</td>
<td>AP</td>
<td>1</td>
</tr>
<tr>
<td>2.</td>
<td>Kerala</td>
<td>2</td>
</tr>
<tr>
<td>3.</td>
<td>WB</td>
<td>1</td>
</tr>
<tr>
<td>4.</td>
<td>KTK</td>
<td>1</td>
</tr>
<tr>
<td>5.</td>
<td>MH</td>
<td>2</td>
</tr>
<tr>
<td>6.</td>
<td>RAJ</td>
<td>1</td>
</tr>
<tr>
<td>7.</td>
<td>UP(E)</td>
<td>1</td>
</tr>
<tr>
<td>8.</td>
<td>TN</td>
<td>1</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td><strong>10</strong></td>
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</tbody>
</table>
Scheme and Syllabus for the Recruitment of Junior Telecom Officers (Telecom)

For Direct Recruitment of Junior Telecom Officers, an objective type Examination of 3 hours duration consisting of following sectional papers will be conducted:

SCHEME

A. Engineering Stream Section - I
B. Engineering Stream Section - II
C. General Ability Test Section - III

1. The standard of paper in Engineering subjects will be that of Engineering Degree Examination of an Indian University.

2. In the general ability test, special attention will be paid to assess the candidate's capacity for general awareness. The standard of paper in general ability test will be such as may be expected of an Engineering Graduate.

3. The syllabus for engineering stream papers will be as given below.

SYLLABUS

SECTION - I

1. Materials and components
   Structure and properties of Electronic Engineering materials, Conductors, Semiconductors and Insulators, Magnetic, Ferroelectric, Piezoelectric, Ceramic, Optical and Superconducting materials. Passive components and characteristics, Resistors, Capacitors and Inductors; Ferrites, Quartz crystal, Ceramic resonators, Electromagnetic and Electromechanical components.

2. Physical Electronics, Electronic Devices and ICs
   Electrons and holes in semiconductors, Carrier Statistics, Mechanics of current flow in a semi-conductor, Hall effect; Junction theory; Different types of diodes and their characteristics; Bipolar Junction transistor; Field effect transistors; Power switching devices like SCRs, CTOs, power MOSFETs; Basics of ICs-bipolar, MOS and CMOS types; Basics of Opto Electronics.
3. **Network theory**


4. **Electromagnetic Theory**

Transmission lines: basic theory, standing waves, matching applications, microstrip lines; Basics of waveguides and resonators; Elements of antenna theory.

5. **Electronic Measurements and instrumentation**

Basic concepts, standards and error analysis; Measurements of basic electrical quantities and parameters; Electronic measuring instruments and their principles of working: analog and digital, comparison, characteristics, applications. Transducers; Electronic measurements of non-electrical quantities like temperature, pressure, humidity etc. Basics of telemetry for industrial use.

6. **Power Electronics**

Power Semiconductor devices, Thyristor, Power transistor, MOSFETs, Characteristics and operation. AC to DC convertors; 1-Phase and 3-phase DC to DC Convertors.

AC regulators. Thyristor controlled reactors, switched capacitor networks.


**SECTION-II**

1. **Analog Electronic Circuits**

2. **Digital Electronic Circuits**

Transistor as a switching element; Boolean algebra, simplification of Boolean functions, Karnaugh Map and applications; IC Logic gates and their characteristics; IC logic families: DTL, TTL, ECL, NMOS, PMOS and CMOS gates and their comparison; Combinational logic circuits; Half adder, full adder; Digital Compartor; Multiplexer Demultiplexer; ROM and their applications. Flip-flops, R-S, J-K, D and T flip-flops; Different types of counters and registers; waveform generators. A/D and D/A convertors. Semiconductor memories.

3. **Control Systems**

Transient and steady state response of control systems; Effect of feedback on stability and sensitivity, Root locus techniques; Frequency response analysis. Concepts of gain and phase margins; Constant-M and Constant-N Nichol's Chart; Approximation of transient response from Constant-N Nichol's Chart; Approximation of transient response from closed loop frequency response; Design of Control Systems, Compensators; Industrial controllers.

4. **Communication systems**

Basic information theory: Modulation and detection in analogue and digital systems; Sampling and data reconstruction. Quantization & Coding; Time division and frequency division multiplexing; Equalisation; Optical Communication: in free space & fibre optic; Propagation of signals at HF, VHF, UHF and microwave frequency; Satellite communication.

5. **Microwave Engineering**

Microwave Tubes and solid state devices, Microwave generation and amplifiers, Waveguides and other Microwave Components and Circuits, Microstrip circuits, Microwave antennas, Microwave Measurements, MASERS LASERS; Microwave Propagation. Microwave Communication Systems-terrestrial and satellite based.

6. **Computer Engineering**

Number Systems; Data representation; Programming; Elements of a high level programming language PASCAL/C; use of basic data structures; Fundamentals of
computer architecture processor design; Control unit design; Memory organization. I/O System Organization. Personal computers and their typical uses.

7. Microprocessors

Microprocessor architecture - Instruction set and simple assembly language programming. Interfacing for memory and I/O. Applications of Microprocessors in Telecommunications and power system.

SECTION-III

General ability test

The candidate’s comprehension and understanding of General English shall be tested through simple exercises. Questions on knowledge of current events and of such matter of everyday observation and experience in their scientific aspects as may be expected of an educated person. Questions will also be included on events and developments in Telecommunications, History of India and Geography. These will be of a nature, which can be answered without special study by an educated person.
Scheme and Syllabus for the Recruitment of Junior Telecom Officers (Civil)

For Direct Recruitment of Junior Telecom Officer (Civil), an objective type Examination of 3 hours duration consisting of following sectional papers will be conducted:

SCHEME

A. Civil Engineering Stream Section - I : 50 questions
B. Civil Engineering Stream Section - II : 50 questions
C. General Ability Test Section - III : 20 questions

1. The standard of paper in Engineering subjects will be that of Engineering Degree Examination of Indian University.

2. In the general ability test, special attention will be paid to assess the candidate's capacity for general awareness. The standard of paper in general ability test will be such as may be expected of an Engineering Graduate.

3. The syllabus for Civil Engineering stream paper will be as given below.

SYLLABUS:

SECTION-I - CIVIL ENGINEERING STREAM

1. BUILDING MATERIAL:
Timber: Different types and species of structural timber, density-moisture relationship, strength in different directions, defects, influence of defects on permissible stress, preservation, dry and wet rots, plywood, codal provision for design.

Bricks: Types, Indian standard classification, absorption, saturation factor, strength in masonry, influence of mortar strength and masonry strength.

Cement: Compounds, different types, setting times, strength.

Cement Mortar: Ingredients, proportions, water demands, mortar for plastering and masonry.

Concrete: Importance of W/C ratio, strength, ingredients including admixtures, workability, testing, elasticity, non-destructive testing mix design method.
2. SOLID MECHANICS
Elastic constants, stress, plane stress, Mohr's circle of stress, strains, plain strain, Mohr's circle of strain, combined stress. Elastic theories of Failure, simple and shear bending, Torsion of circular and rectangular section and simple members.

3. STRUCTURAL ANALYSIS

4. DESIGN OF STEEL STRUCTURES

5. DESIGN OF CONCRETE AND MASONRY STRUCTURES.
Limit state design for bending, shear, axial compression and combined forces. Codal provisions for slabs, beams, walls and footings. Working stress method of design of R.C. members.

Principles of prestressed concrete design, material, method of prestressing losses. Design of simple members and determinates structures. Introductions to prestressing of indeterminate structures.

Design of brick masonry as per I.S. codes.

6. CONSTRUCTION PRACTICE, PLANNING AND MANAGEMENT.
Concreting Equipment:
Weight batcher, Mixer, vibrator, batching plant, concrete pump.

Cranes, hoists, lifting equipment.
Earthwork Equipment:
Power shovel, hoe, dozer, dumper, trailers and tractors, rollers, sheep foot rollers, pumps.

Construction, planning and Management:
Bar chart, linked bar chart, work break down structures, Activity-on-arrow diagrams. Critical path, probabilistic activity durations; Event-based networks. PERT network: Time-cost study, crashing; Resource allocation.

SECTION- II - CIVIL ENGINEERING STREAM

1. (a) FLUID MECHANICS, OPEN CHANNEL, PIPE FLOW:

(b) HYDRAULIC MACHINES AND HYDROPOWER
Centrifugal pumps, performance parameters, scaling, pumps in parallel, Reciprocating pumps, air vessels, performance parameters;

2. (a) HYDROLOGY:
Hydrological cycle, precipitation and related data analysis, PMP, unit and synthetic hydrographs, Evaporation and transpiration, floods and their management, PMG, Streams and their gauging, River morphology. Rooting of floods, Capacity of reservoirs.

(b) WATER RESOURCES ENGINEERING:
Water resources of the globe: Multipurpose uses of Water, Soil Plant water relationships, irrigation systems, water demand assessment, Storage and their yields, ground water yield and well Hydraulics, Water logging, drainage design, Irrigation revenue, Design of rigid boundary canals, Lacey' and Tractive force concepts in canal design, lining of canals; Sediment transport in canals; Non-Overflow and overflow sections of gravity dams and their design,
Energy dissipaters and tail water rating, Design of head works, distribution work, falls, cross-drainage work, outlets, River training.

ENVIRONMENT ENGINEERING

3. **(a)WATER SUPPLY ENGINEERING.**

Sources of supply, yield, design of intakes and conductors, Estimation of demand, Water quality standards, Control of water born diseases. Primary and secondary treatment, detailing and maintenance of treatment units. Conveyance and distribution systems of treated water, leakage and control, Rural water supply, Institutional and Industrial water supply.

**(b) WASTE WATER ENGINEERING**

Urban rain water disposal, system of sewage collection and disposal, Design of sewers and sewerages systems, pumping, Characteristic of sewage and its treatment, Disposal of products of sewage treatment, stream flow rejuvenation, Institutional and industrial sewage management, plumbing system, Rural and semi-urban sanitation.

**(c) SOLID WASTE MANAGEMENT**

Sources, classification, collection and disposal, Design and Management of landfills.

**(d) AIR AND NOISE POLLUTION AND ECOLOGY.**

Sources and effects of air pollution, monitoring of Air pollution, Noise-pollution and standards; Ecological Chain and balance, Environmental assessment.

4. **(a)SOIL MECHANICS**

Properties of soils, classification and interrelationship, Compaction behavior, method of compaction and their choice, Permeability and seepage, flow nets, Inverter filters, Compressibility and consolidation, shearing resistance, stresses and failure, SO testing in laboratory and in-situ, Stress path and applications, Earth pressure theories, stress distribution in soil, soil exploration, samplers, load tests, penetration tests.
(b) FOUNDATION ENGINEERING

Type of foundations, Selection criteria, bearing capacity, settlement, laboratory and field test, Types of piles and their design and layout, Foundations on expansive soils, swelling and its prevention, foundation on swelling soils.

5. (a) SURVEYING

Classification of surveys, scales, accuracy, Measurement of distances-direct and indirect methods, optical and electronic devices, Measurement of directions, prismatic compass, local attraction, Theodolites-types Measurement of elevations, Spirit and trigonometric leveling, Relief representation, Contours, Digital elevation modeling concept, Establishment of control by triangulations and traversing measurements and adjustment of observations, computation of coordinates, Field astronomy, concept of global positioning system, Map preparation by plane tabling and by photogrammetry, Remote sensing concepts, map substitutes.

(b) TRANSPORTATION ENGINEERING

Planning of highway systems, alignment and geometric design, horizontal and vertical curves, grade separation, Materials and construction methods for different surfaces and maintenance, Principles of pavement design, Drainage.

Traffic surveys, intersections, signalling, Mass transit systems, accessibility, networking.

Planning of railway systems, terminology and designs, relating to gauge, track controls, transits, rolling stock, tractive power and track modernization, Maintenance Appurtenant works, Containerisation.

SECTION-III - GENERAL ABILITY TEST

The candidate's comprehension and understanding of general English shall be tested through simple exercises. Questions on knowledge of current events and of such matter of everyday observation and experience in their scientific aspects as may be expected of an educated person. Questions will also be included on events and developments in Tele Communications, History of India and Geography. These will be of a nature, which can be answered without special study by an educated person.
SCHEME AND SYLLABUS FOR DIRECT RECRUITMENT OF JUNIOR TELECOM OFFICERS (ELECTRICAL) THROUGH OPEN COMPETITIVE EXAMINATION IN BSNL

For direct recruitment of JTO (Electrical), an objective type examination of one paper of three hours duration consisting of following sections will be conducted:

Section-I: Electrical Engineering: 50 Questions (2 Marks Each)
Section-II: Electrical Engineering: 50 Questions (2 Marks each)
Section-III: General Awareness: 20 Questions (2 Marks Each)

1. The standard of paper in engineering subject will be that of Engineering Degree Examination of Indian University.
2. In the general ability test, special attention will be paid to assess the candidate’s capacity for general awareness. The standard of paper in general ability test will be such as may be expected of an Engineering Graduate.
3. The syllabus for engineering stream paper will be given below.

SYLLABUS

SECTION –I  ELECTRICAL ENGINEERING

1. EM Theory


2. Electrical Materials


3. Electrical Circuits

4. **Measurements and Instrumentation**


5. **Control System**

Transient and steady state response of control system; Effect of feedback on stability and sensitivity. Root locus techniques; Frequency response analysis. Concept of gain and phase margins; Constant-M and Constant-N Nichol’s Chart; Approximation of transient response from constant N Nichol’s chart; Approximation of transient response from closed loop frequency response; Design of Control system, Compensators; Industrial controllers.

**SECTION-II ELECTRICAL ENGINEERING**

1. **Electrical Machines and Power Transformers**


2. **Power systems**


3. **Analog and Digital Electronics and Circuits**

Semiconductor device physics, PN junctions and transistors, circuit models and parameters FET, Zener, tunnel, Schottky, photodiodes and their application, rectifier circuits, voltage regulators and multipliers, switching behavior of diodes and transistors.

Small signal amplifiers, biasing circuits, frequency response and improvement, multistage amplifiers and feed-back amplifiers. Operational amplifiers wave shaping circuits. Multivibrators and flip-flops and their applications. Digital logic gate families, universal gates-combination circuits for arithmetic and logic operational, sequential logic circuits. Counters, registers, RAM and ROMs.
4. **Microprocessor.**
   Microprocessor architecture-instruction set and simple assembly language programming. Interfacing for memory and I/O. Application of Micro-processor in power system.

5. **Communication Systems**
   Type of modulation; AM, FM and PM. Demodulations. Noise and bandwidth considerations. Digital communication system. Pulse code modulation and demodulation. Carrier communication. Frequency division and time division multiplexing.

6. **Power Electronics**
   Power Semiconductor devices. Thyristors, Power transistor, GTO’s and MOSFETS. Characteristic and operation. AC to DC Convertors; Single phase and three phase DC to DC Convertors; AC Regulators. Thyristors controlled reactors; switched capacitor network. Invertors; single phase and three phase. Pulse width modulation. Sinusoidal modulation with uniform sampling. Switched mode power supplies.

**SECTION –III GENERAL ABILITY.**

The candidate’s comprehension and understanding of general English shall be tested through simple exercises. Questions on knowledge of current events and of such matter of everyday observation and experience in their scientific aspects as may be expected of an educated person. Question will also be included on events and developments in Tele Communications, History of India and Geography. These will be of a nature, which can be answered with our special study by an educated person.